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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/863,223	05/24/2001	Chung-Shi Liu	TS00-488	9868	
20112	112 7590 12/31/2002 EORGE O. SAILE & ASSOCIATES		EXAMINER		
28 DAVIS AVENUE POUGHKEEPSIE, NY 12603			COLEMAN, WILLIAM D		
			ART UNIT	PAPER NUMBER	
			2823		
			DATE MAILED: 12/31/2002	DATE MAILED: 12/31/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

***		Application No.	Applicant(s)			
Office Action Summary		09/863,223	LIU ET AL.			
		Examiner	Art Unit			
		W. David Coleman	2823			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status						
1)🛛	Responsive to communication(s) filed on 17.5	September 2002 .				
2a) <u></u> □	This action is FINAL . 2b)⊠ Th	is action is non-final.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213. Disposition of Claims						
•	Claim(s) 1-22 is/are pending in the application	l.				
4a) Of the above claim(s) <u>13-22</u> is/are withdrawn from consideration.						
5) Claim(s) <u>5-12</u> is/are allowed.						
6)⊠ Claim(s) <u>1-4</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) 🗆 -	The specification is objected to by the Examine	r.				
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
11) ☐ The proposed drawing correction filed on is: a) ☐ approved b) ☐ disapproved by the Examiner.						
If approved, corrected drawings are required in reply to this Office action.						
12) The oath or declaration is objected to by the Examiner.						
Priority under 35 U.S.C. §§ 119 and 120						
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) ☐ All b) ☐ Some * c) ☐ None of:						
1. Certified copies of the priority documents have been received.						
	2. Certified copies of the priority documents have been received in Application No					
 Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).						
a) The translation of the foreign language provisional application has been received.						
15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.						
Attachment(s)						
2) Notic	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449) Paper No(s) 2	5) Notice of Information	ary (PTO-413) Paper No(s) al Patent Application (PTO-152)			
U.S. Patent and T	rademark Office					

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DETAILED ACTION

Election/Restrictions

1. Claims 13-22 withdrawn from further consideration pursuant to 37 CFR 1.142(b), as being drawn to a nonelected invention, there being no allowable generic or linking claim.

Applicant timely traversed the restriction (election) requirement in Paper No. 4.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

- 3. The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) do not apply to the examination of this application as the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).
- 4. Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by Tsai et al., U.S. Patent 6,319,814 B1..
- 5. Tsai discloses a semiconductor process as claimed. See FIGS. 2A-2H, and column 2, lines 23-30, where <u>Tsai</u> teaches a process for filling a via hole in a layer of FSG, having an upper surface.

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comprising the sequential steps of:

depositing the layer of FSG on a substrate;

depositing a layer of USG on the upper surface-,

patterning and then etching said USG and FSG layers, whereby a via hole, having walls and extending as far as the substrate, is formed;

depositing a seed layer on the walls of the via hole; and overfilling the via hole with a material,, removing said material until by means of CMP aid USG layer is reached.

Claim Rejections - 35 USC § 103

- 6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 7. Claims 2 and 3 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tsai et al., U.S. Patent 6,319,814 B1.
- 8. Pertaining to claims 2 and 3, <u>Tsai</u> fails to teach the thickness of the FSG with the claimed atomic percentage of fluorine and the thickness of the USG as claimed. Given the teaching of the references, it would have been obvious to determine the optimum thickness, temperature as well as condition of delivery of the layers involved. See *In re Aller, Lacey and Hall* (10 USPQ 233-237) "It is not inventive to discover optimum or workable ranges by routine experimentation. Note that the specification contains no disclosure of either the critical nature of the claimed ranges or any unexpected results arising therefrom. Where patentability is said to be

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based upon particular chosen dimensions or upon another variable recited in a claim, the Applicant must show that the chosen dimensions are critical. *In re Woodruff*, 919 f.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed. Cir. 1990).

Any differences in the claimed invention and the prior art may be expected to result in some differences in properties. The issue is whether the properties differ to such an extent that the difference is really unexpected. *In re Merck & Co.*, 800 F.2d 1091, 231 USPQ 375 (Fed. Cir. 1986)

Appellants have the burden of explaining the data in any declaration they proffer as evidence of non-obviousness. *Ex parte Ishizaka*, 24 USPQ2d 1621, 1624 (Bd. Pat. App. & Inter. 1992).

An Affidavit or declaration under 37 CFR 1.132 must compare the claimed subject matter with the closest prior art to be effective to rebut a prima facie case of obviousness. *In re Burckel*, 592 F.2d 1175, 201 USPQ 67 (CCPA 1979).

- 9. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Tsai et al, U.S. Patent 6,319,814 B1 as applied to claims 1, 2 and 3 above, and further in view of Lee et al., U.S. Patent 6,103,601.
- 10. Tsai fails to teach the process described in claim 1 wherein the step of depositing the USG layer further comprises using PECVD from silane or TEOS at about 400 °C. Lee teaches the process wherein the step of depositing the USG layer further comprises using PECVD from silane or TEOS at about 400 °C. In view of Lee, it would have been obvious to one of ordinary skill in the art to incorporate the process step of Lee into the Tsai semiconductor process because the USG layer can be deposited in a separate processing chamber from the FSG layer (column 7, lines 51-63).

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Claim Rejections - 35 USC § 112

11. Claim 1 rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to

particularly point out and distinctly claim the subject matter which applicant regards as the

invention. Applicant claims depositing a layer of USG on the upper surface, it is not clear if the

upper surface is the substrate or the FSG layer.

12. Correction is required.

Allowable Subject Matter

13. Claims 5-12 allowed.

14. The following is an examiner's statement of reasons for allowance: prior art does not

anticipate nor render obviousness as to a process for forming a single damascene connector,

comprising the sequential steps of:

providing a partially completed integrated circuit and then depositing thereon a layer of

FSG having an upper surface'

depositing a layer of USG on said upper surface;

on the USG layer, depositing a layer of silicon oxynitride for use as an anti-reflection

coating;

patterning and then etching said oxynitride, USG, and FSG layers, thereby forming a via

hole extending as far as said integrated circuit;

depositing a barrier layer on the walls of the vial hole;

depositing a copper seed layer on the said barrier layer;

overfilling the vial hole with copper; and

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by means of CMP, removing the copper until said USG layer is reached, thereby forming said damascene connector.

15. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

16. Any inquiry concerning this communication or earlier communications from the examiner should be directed to W. David Coleman whose telephone number is 703-305-0004. The examiner can normally be reached on 9:00 AM-5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on 703-306-2794. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-308-7721 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

W. David Coleman

Examiner

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WDC December 27, 2002